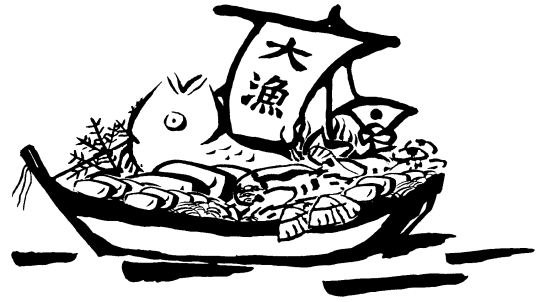


Call for Participation SASIMI 2012

THE 17TH WORKSHOP ON
SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

March 8–9, 2012

B-con Plaza, Beppu, Oita, Japan



In cooperation with: Institute of Electrical and Electronics Engineers (IEEE), Fukuoka Section
IEEE Circuits and Systems Society Japan Chapter, Fukuoka Chapter
The Institute of Electronics, Information and Communication Engineers (IEICE)
Information Processing Society of Japan (IPSJ)

Supported by: Semiconductor Technology Academic Research Center (STARC)

Aims of the Workshop:

This workshop provides an interchange forum on system design, design experiences, EDA, and design methodologies for both of industry and academy. Many researchers of Asia, U.S. and European countries in the area of hardware design methodology have supported this workshop and made SASIMI one of the most famous events in this research area. The workshop gives a good opportunity for discussion on advanced work and research. Researchers in the field of system design, VLSI design, VLSI CAD, and VLSI technologies are welcome to this workshop.

Keynote Speeches:

- Subhasish Mitra (Stanford Univ., USA): “Robust System Design: Overcoming Complexity and Reliability Challenges”
- Jason Cong (UCLA, USA): “Parallelization, Customization and Automation”

Invited Talks:

- Rob van Schaijk (IMEC / Holst Centre, The Netherlands): “Energy Harvesting for Self Powered Sensor Systems — Case Study: Vibration Energy Harvesting for ‘Intelligent Tire’ Application —”
- Sungjoo Yoo (POSTECH, Korea): “Innovating the SoC Design for Emerging Memory Technologies”
- Takahide Yoshikawa (Fujitsu Laboratories Ltd., Japan): “K computer: Challenges Making the Superior Quality Interconnect”

Panel Discussion:

“Challenges for Future System Design and Verification”

- Organizer: Shinji Kimura (Waseda Univ., Japan)
- Panelists: Jason Cong (UCLA, USA), Subhasish Mitra (Stanford Univ., USA), Sungjoo Yoo (POSTECH, Korea), Rob van Schaijk (IMEC/Holst Centre, The Netherlands), Takahide Yoshikawa (Fujitsu Laboratories Ltd., Japan)

Regular Poster Sessions:

This workshop is designed to maximize interaction and informal interchange. While full or short papers are to be published for all contributors, a majority of the presentations are of a poster-board format. That is, each author gives a very short introduction followed by an extended period for informal one-to-one discussions. Many “fresh” papers are to be presented in regular poster sessions on the topics including System Level Design, Logic Design, Physical Design, and Design Experiences.

Registration:

All the participants, including keynote/invited speakers, panelists, moderators, and session chairs, are required to make registration. The deadline for advance registration (via WWW) is February 8, 2012.

For More and Latest Information: <http://sasimi.jp/>

Conference Venue:

Beppu City, located on Kyushu, the most southwesterly island in Japan, is an area blessed with rich *onsen* (*hot spring*) sources. There are 2,909 *hot spring* vents within the city, and you can see steam from the hot spring all over the city. There are special geothermal pits called *Jigoku*, which means *hell*, and the *Jigoku tour* is a good attraction for the tourists. *Beppu* has a variety of *fresh seafood* from Beppu Bay and Bungo Channel and also has many unique local cuisines and good local liquors in the area. *Beppu* attracts over 12 million Japanese and foreign visitors each year.

Questions should be directed to:

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