THE 20TH WORKSHOP ON SYNTHESIS AND SYSTEM INTEGRATION
OF MIXED INFORMATION TECHNOLOGIES

October 24-25, 2016, Kyoto Research Park, Kyoto, Japan

In cooperation with:
IEEE Council on Electronic Design Automation (CEDA)
IEEE Circuits and Systems Society (CAS), Kansai Chapter
IEEE Computer Society (CS), Kansai Chapter
The Institute of Electronics, Information and Communication Engineers (IEICE)
Information Processing Society of Japan (IPSJ)

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Kyoto City and the Kyoto Convention & Visitors Bureau

Workshop Program
October 24, 2016
9:00-9:20 Opening (Buzz Hall)
9:20-10:20 Keynote Speech (Buzz Hall) (Chair: Nagisa Ishiura (Kwansei Gakuin Univ.))
Giovanni De Micheli (EPFL, Lausanne, Switzerland)
“Cyber-Medical Systems: Requirements, Components and Design”
10:20-11:50 Poster Session I (Buzz Hall/Banquet Hall)
(Co-Chairs: Yoshinori Takeuchi (Osaka Univ.), Kazuya Tanigawa (Hiroshima City Univ.))
11:50-13:20 Lunch Break
13:20-14:10 Invited Talk I (Buzz Hall) (Chair: Mineo Kaneko (JAIST))
David Chen (ARM, China)
“Computing in the IoT Era, Opportunities and Challenges”
14:10-15:40 Poster Session II (Buzz Hall/Banquet Hall)
(Co-Chairs: Masato Inagi (Hiroshima City Univ.), Seiya Shibata (NEC))
15:40-17:40 20th Memorial Ceremony (Buzz Hall)
15:55-17:40 Memorial Panel Discussion (Buzz Hall)
“Past and Future 25 Years of Synthesis and System Integration”
Moderator: Isao Shirakawa (University of Hyogo)
Panelists: Giovanni De Micheli (EPFL, Switzerland),
Youn-Long Lin (National Tsing Hua University, Taiwan),
Ren-Song Tsay (National Tsing Hua University, Taiwan),
Peter Marwedel (Technical University of Dortmund, Germany)
Organizer: Nagisa Ishiura (Kwansei Gakuin Univ.)
18:00-20:00 Banquet (Atrium)

October 25, 2016
9:10-10:00 Invited Talk II (Buzz Hall) (Chair: Kiyoharu Hamaguchi (Shimane Univ.))
Alan Manteeth (University of Arkansas, U.S.A.)
“Wide Bandgap Analog and Mixed-signal IC Design for Advanced Power Electronics”
10:00-11:50 Poster Session III (Buzz Hall/Banquet Hall)
(Co-Chairs: Wenxing Zhu (Fuzhou Univ., China), Hideki Takase (Kyoto Univ.))
11:50-13:20 Lunch Break
13:20-14:10 Invited Talk III (Buzz Hall) (Chair: Kiyoharu Hamaguchi (Shimane Univ.))
Ren-Song Tsay (National Tsing Hua University, Taiwan)
“The Challenges and Future of Electronic-System Level Design Automation”
14:10-16:00 Poster Session IV (Buzz Hall/Banquet Hall)
(Co-Chairs: Tsuyoshi Matsumoto (Ishikawa Tech. College), Xin Jin (Tsinghua Univ., China))
16:00-16:50 Invited Talk IV (Buzz Hall) (Chair: Mineo Kaneko (JAIST))
Takeki Kohno (The University of Tokyo)
“Qualitative-modeling-based design for silicon neuronal networks”
16:50-17:00 Closing (Buzz Hall)

Venue Map / Room Assignment

This program is supported by a subsidy from Kyoto City and
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