

# Call for Papers

# SASIMI 2016

THE 20TH WORKSHOP ON  
SYNTHESIS AND SYSTEM INTEGRATION  
OF MIXED INFORMATION TECHNOLOGIES

**October 24-25, 2016**

**Kyoto Research Park, Kyoto, Japan**



This workshop will provide an interchange forum on system design, design experiences, EDA, and design methodologies for both of industry and academy. Presentations on theoretical aspects, practical issues, case studies and applications are encouraged. The workshop gives an opportunity for presentation and discussion of advanced work and research. Works in progress and new ideas are also welcome. Special sessions for hot topics will be provided. Proposals for the topics are welcome.

**Areas of Interest** include, but are not limited to:

- System Design and Design Experiences, Industry Experiences
- Software/Hardware Design for Embedded Systems, Cyber-Physical Systems, IoT, etc.
- New Design Methodologies for MEMS, Bioengineering, Automobile, Environment, etc.
- Reconfigurable System Design
- Design for Manufacturability, Reliability, Security, etc.
- Behavioral/Logic/Layout Synthesis
- Test, Verification and Simulation
- Analog and Mixed-Signal Design

## **Submission of Papers:**

Prospective authors are invited to submit original papers of 3 to 6 pages (for full papers\*) or of 2 pages (for short papers), electronically via the web site below. Detailed instructions for paper submission will be available at the web site. Official language is English.

\* Best Paper Awards will be selected from full papers.

## **Key Dates:**

<b>Submission due date</b>	<b>June 3 (Fri), 2016</b>
<b>Notification of Acceptance</b>	<b>August 5 (Fri), 2016</b>
<b>Final paper due date</b>	<b>September 9 (Fri), 2016</b>

## **Questions should be directed to:**

SASIMI 2016, c/o Prof. Mineo Kaneko

School of Information Science, Japan Advanced Institute of Science and Technology  
1-1 Asahidai, Nomi-shi, Ishikawa 923-1292, Japan

E-mail: sasimi16@sasimi.jp

**For More and Latest Information: <http://sasimi.jp/>**